

Firmware Engineer (synchronization system)

General information

Place of employment: Laboratory of High Energy Physics, JINR, Dubna

Published Date: 22 March 2021

Duration of the contract: 3 years

Deadline for application: 01 July 2021

Share of work: full time, 8h

Remuneration: 800,000 – 2,700,000 roubles per year (depending on experience, qualifications and interview results), income tax of 13% is applied (regulations of the Russian Federation); employer provides family insurance and helps with housing provision

Desired level of education: higher professional education

Experience required: 5 years in electronics development

Job description

The successful candidate will be a member of the Accelerator division at VBLHEP JINR. The division work is focused on the development of the NICA accelerator facility. ***The candidate will act as a senior electronic engineer of the synchronization system with the abilities of leading the team, setting the tasks, making analysis of existing problems and proposing solutions in this area, confident in making strategic decisions on the development of the system.***

Skills

Knowledge of digital circuitry, programming techniques and basic design flows for systems based on FPGA at the professional level (including Verilog and VHDL hardware description languages), microcontrollers programming. Knowledge of CAD for tracing printed circuit boards at the professional level.

Knowledge of English at the level of negotiating with foreign specialists and participating in international conferences. Knowledge of tools for planning the work process.

Constraints and risks

Shift work and weekend work may be necessary. The work will be carried out at accelerator facilities whereby the necessary authorizations will be issued following the annual medical examination arranged by the employer.

How to apply

Applicants should submit a detailed CV, a summary of research interests, a list of publications, highlighting representative publications, and arrange to have at least two reference letters sent by the referees to the email address below. These documents should be addressed to Prof. V.D. Kekelidze and sent to Anna Rassadova, rassadova@jinr.ru.

Short-listed candidates will be invited to an interview, remotely or in person.